

CIR-S1DUA E40512

DDR1 DIMM 400MHz 512MB

Description

The CIR-S1DUT E40512 is 64M words X 64 bits, 1 rank Double Data Rate (DDR) SDRAM unbuffered modules, (DIMMs), mounting 8 pieces of 512M bit DDR SDRAM (64Mx8) sealed in TSOPII package.

The CIR-S1DUT E40512 incorporates SPD (serial presence detect). SPD function is implemented via a serial 2,048-bit EEPROM. The first 128 bytes of serial PD data are programmed by Promos to identify DIMM type, capacity and other the information of DIMM and the last 128 bytes are available to the customer.

Specifications

Density	512MB
Pin Count	184pin
Type	Unbuffered
Dimensions	133.35mm x 29.464mm
ECC	Non-ECC
Component Config	64M x 8 bit
Data Rate	400 MHz
CAS Latency	3
Voltage	2.6V
PCB Layers	6
Operating Temp.(TCASE)	0°C~+70°C
Module Ranks	Single Rank

Features

- JEDEC industry standard 184-pin Unbuffered dual in-line memory module (DIMM)
- Fast data transfer rates: 400MHz (max)
- Power supply: 2.6V +/- 0.1V Vdd and Vddq
- SSTL_2 interface
- Double-data-rate architecture; two data transfers per clock cycle
- Bidirectional data strobe(DQS)
- DLL aligns DQ and DQS transition with CK transition
- Differential clock inputs (CK and /CK)
- Programmable /CAS Latency (CL): 2.5 / 3
- Programmable Burst Length: (2 , 4 , 8)
- Auto refresh and self refresh
- Edge aligned data output, center aligned data input
- Serial presence detect (SPD) with EEPROM
- All of Lead-Free products are compliant for RoHS

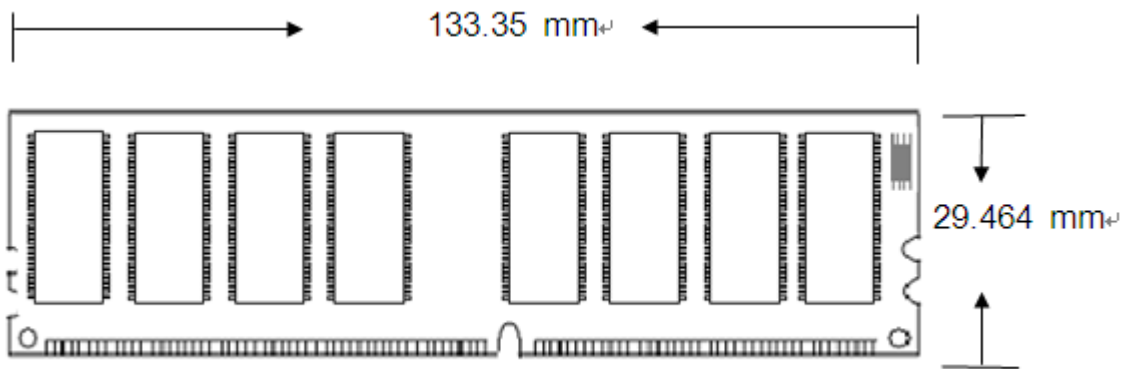


Speed Grade

Frequency Grade	Data Transfer Rate	CAS Latency Support		CL-tRCD-tRP
		CL2.5	CL3	
DDR-400	PC-3200	266	333/400	3-3-3

Package Dimensions

Unit: mm



Tolerances : $\pm 0.15\text{mm}$ unless otherwise specified

